MODEL NUMBER ASSIGNMENT SHEET (MNAS) FERRETRAC CLOSED-LOOP BANDPASS FILTER

MODEL NUMBER

FT 2531	31 вр 🗶 вк 🗌				CUSTOMER CUSTOMER P/N			JPL CUST TELEDYNE X		
RF SPECIFICATIONS				CON	CONTROLLING DOCUMENT CUSTOMER SCD NO				-EDYNE X	
				• 4	OOOTOME	K OOD NO				
NUMBER OF STAGES				TO 01	TOD 0.6	_				
TUNING RANGE (GHz) START BANDWIDTH-ON-FREQUENCY (MHz MIN)				TO S 1	TOP 26.	5				
INSERTION LOSS (dB MAX)]						
RIPPLE & SPURIOUS (dB MAX)			,							
PASSBAND VSWR (MAX)			•	X	BEST PT			OTHER		
HALF IF REJECTION (dB MIN)			•	22	DEGITI					
REFERENCE REJECTION (dB MIN)				1				_		
IMAGE REJECTION (dB MIN)			-	_						
HARMONIC REJECTION (dB MIN)			-							
INPUT LIMITING POWER (dBm MIN)			,							
INPUT PWR, NO DAMAGE (dBm MAX)			K) +30							
	0	AL SP TO +6		<u>ATIONS</u>	СОММ	x	RUGGED			
	ALOG						DIGITAL	<u>X</u>		
TUNING VOLTAGE(V	-		TO			NO OF B		12	2.3	
	+15	VDC @	400	mA MAX		START WO		All 0's [=	
	- 15	VDC @	1400	mA MAX		STOP WC		All 1's [3]	
	+5	VDC @	100	mA MAX	LATC	HING (Y OR	RN)	Y		
HEATER POWER	28 120			mA MAX		ATE @ 25°0	;			
POWER INPUT TERMINALS SOLDER PINS			IS L	CONN	ECTOR	X				
OUTLINE 13031	11 [4]			QUA	LITY LEVEL	Comme	ercial			

[3] Level Triggered Latch

[4] Pin 4 will be used for D/A latch.

[1] 400MHz minimum; with a design goal of 500MHz minimum.

[2] Filter closed-loop center freq. may be mistuned (high) at high end of band to achieve this rejection at the reference frequency.

OTHER SPECS